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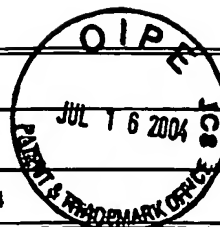
List of Documents
Cited by Applicant

(Use several sheets if necessary)

Applicant: Angelo VISCONTI

Filing Date: February 19, 2004

Group: 2180



U.S. PATENT DOCUMENTS

| Ex'rs In'l | | Document Number | Date | Name | Class | Sub- class | Filing Date, if applicable |
|---------------|-----|--------------------|---------------|-------------|-------|---------------|-------------------------------|
| TL | AA1 | 5,926,409 | July 20, 1999 | Engh et al. | — | — | |
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FOREIGN PATENT DOCUMENTS

| | | Document Number | Date | Country | Class | Sub- class | Trans'l'n Yes/No |
|----|-----|--------------------|-----------------|---------|-------|---------------|------------------|
| TL | AA2 | EP 0 763 828 | March 19, 1997 | Europe | — | — | Yes |
| TL | AA3 | EP 0 908 895 | April 14, 1999 | Europe | — | — | Yes |
| TL | AA4 | WO 01/63613 | August 30, 2001 | PCT | — | — | Yes |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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| TL | AA5 | Roberto Versari et al., "Optimized Programming of Multilevel Flash Eeproms", ICECS 2001: 8 th IEEE International Conference on Electronics, Circuits and Systems, Vol. 2, September 2, 2001, pp. 945-948, XP010563149. |
| TL | AA6 | David Esseni et al., "Trading-Off Programming Speed and Current Absorption in Flash Memories with the Ramped-Gate Programming Technique, IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pp. 828-834, XP001131051. |
| TL | AA7 | European Search Report dated July 25, 2003 for European Application No. 03425096.9. |

Examiner:

Wan

Date Considered:

11/23/05

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